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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Application Number 10/529,675 01.09.2003 Filing Date First Named Inventor Georg Bogner 2875 Art Unit Negron Examiner Name

(use as many sheets as necessary)

2002P07763WOUS / KEL / STC Sheet of **Attorney Docket Number**

-		Document Number	Publication Date	Name of Patentee or Applicant of	Pages, Columns, Lines, Where Relevan Passages or Relevant Figures Appear	
Examiner Initials *	No."	Number - Kind Code ² (if known)	MM-DD-YYYY	Cited Document		
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IN		EP 0 921 568 A2	06-09-1999	Matsushita Electric Works, Ltd.	fig. 2, "circuit parts" 12		
		DE 100 08 203 A1 JP2001274463 Abstract	08-30-2001	Vishay Semiconductor GmbH	abstract and figures		
		DE 200 07 730 U1	09-07-2000	LITE-ON ELECTRONICS INC.	page 4 - line 1		
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		GB 2 361 581 A	10-24-2001	LITE-ON ELECTRONICS INC.	Abstract Page 4, line 14 – page 6, line 13 Page 8, line 6 - page 9, line 12 Figures 2A, 2D, 2E, 3D, 3F, 4A		
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Examiner Signature	/Ismael Negron/	Date Considered	12/08/2006

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